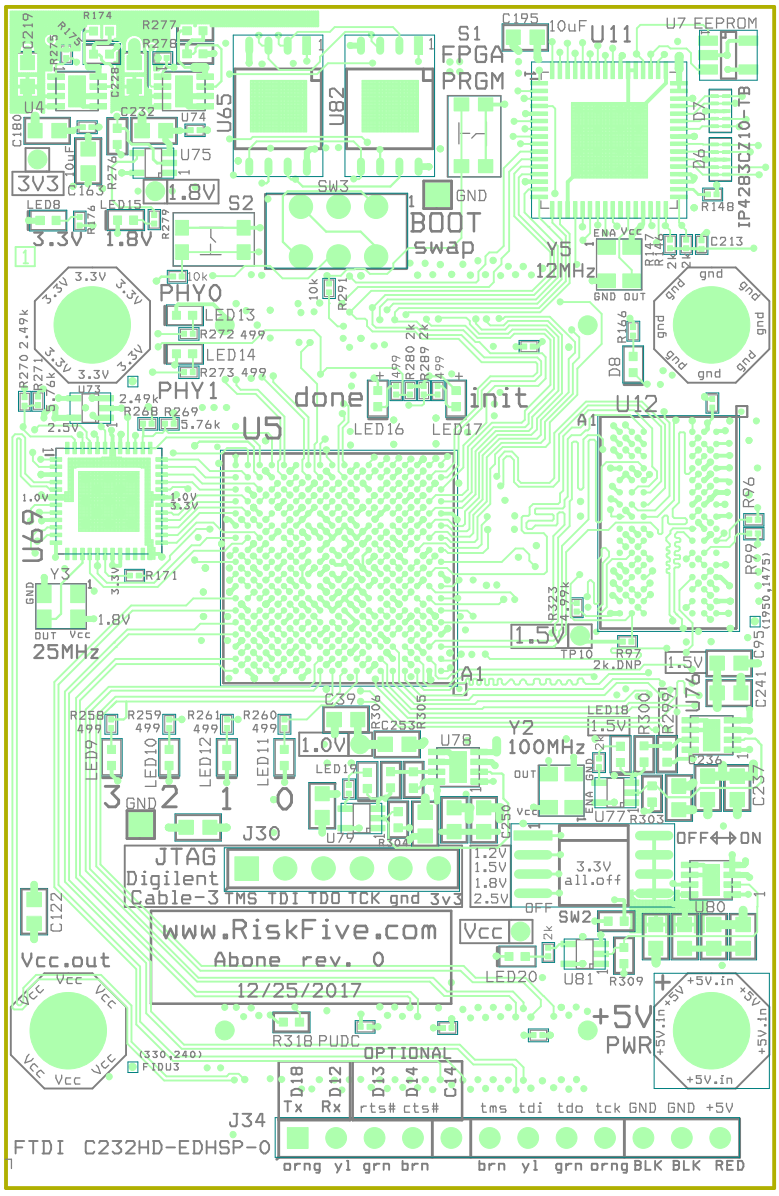

S1
FPGA
PRGM
U11
U7 EEPROM
U65
U82
U4
U75
3V3
1.8V
S2
SW3
BOOT
swap
GND
Y5
12MHz
PHY0
PHY1
LED13
LED14
LED16
LED17
done
init
U5
U12
U69
Y3
25MHz
1.5V
TP10
R97
2k.DNP
U76
Y2
100MHz
U78
U79
U77
J30
JTAG
Digilent
Cable-3
TMS TDI TDO TCK gnd 3v3
3.3V
all.off
OFF ON
SW2
U80
U81
LED20
Vcc
Vcc.out
www.RiskFive.com
Abone rev. 0
12/25/2017
R318 PUDC
OPTIONAL
+5V
PWR
J34
FTDI C232HD-EDHSP-0
orng yl grn brn
brn yl grn orng BLK BLK RED